

Figure 8.

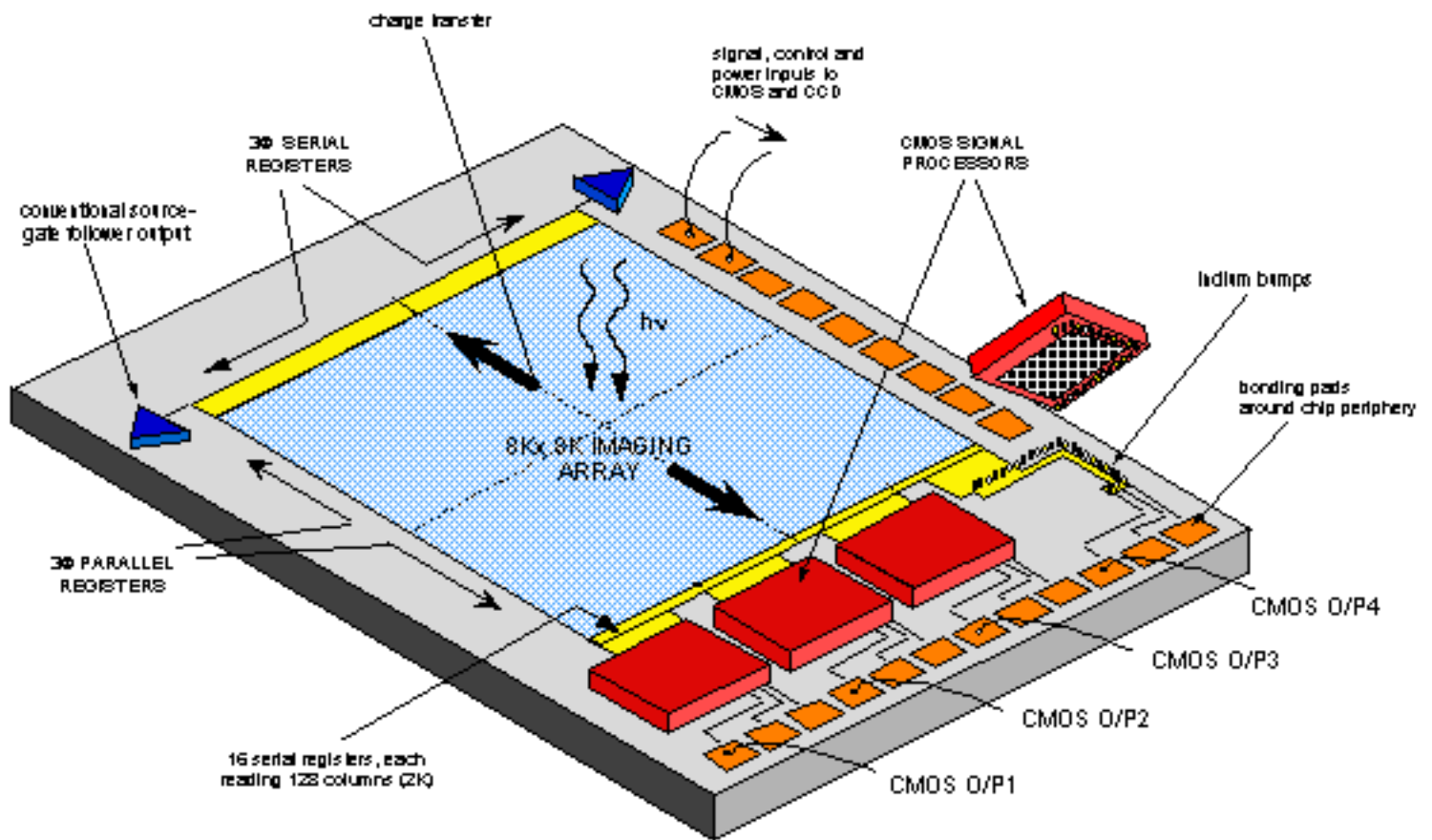


Figure 8: A schematic showing the GSFC/JPL hybrid CCD concept (Courtesy M. Jhabula, GSFC)