

Figure 3

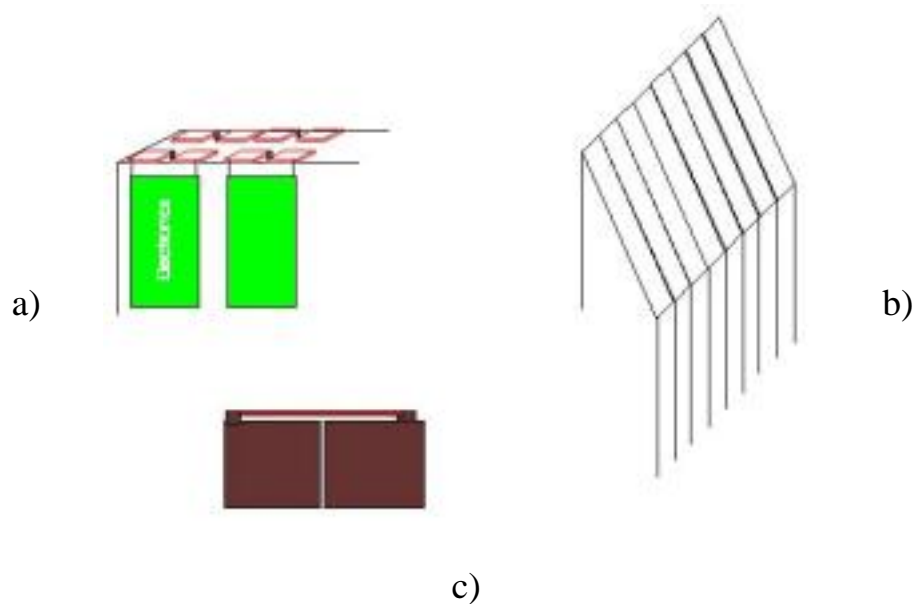


Fig. 3 “Book” geometry for megapixel arrays: a) pixels are deposited in single or double rows on the edges of substrates, and superconducting electronics (including ADC) occupies the larger faces of individual substrates, which then are collected into a stack (b). Sensor occupies a smaller space between absorbers, as shown in (c).